

**U.S. UTILITY Patent Application**

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**O.I.P.E.**

**PATENT DATE**

SCANNED: L.H.4 - O.A. AL.4

APPLICATION NO.  
09/988631

CONT/PRIOR

**CLASS**  
**714**

**SUBCLASS**  
711

ART UNIT  
2133

**EXAMINER**

## APPLICANTS

Joseph Ku

**TITLE**

## Field repairable embedded memory in system-on-a-chip

PTO-2040  
12/99

## ISSUING CLASSIFICATION

[illegible]☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____  <input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
			Amount Due	Date Paid
	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	

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